

Power Factor Correction of AC to DC Converter Using Voltage Pulse Width Modulation (P.W.M) Techniques

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DOI: [10.36347/sjet.2020.v08i09.001](https://doi.org/10.36347/sjet.2020.v08i09.001)

| Received: 06.09.2020 | Accepted: 15.09.2020 | Published: 24.09.2020

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Abstract

Original Research Article

Power factor correction is a way of counteracting the undesirable effects of electric load that create power factor less than unity. The word “converter” in wider perspectives include all the devices that convert electrical energy from one form to another, e.g. inverters, rectifiers or power pack. The term can also denote frequency converter and/or circuit that beat signals together to form a new signal frequency. Pulse Width Modulation (P.W.M) is a method of varying the mark-to-space ratio of the output voltage waveform during a cycle so as to minimize the magnitude of harmonics in the output. Modulation may be done by using transistors as switches instead of thyristors because transistors have much higher switching frequencies, thus, leading to improve and more efficient operation of the thyristors with switching time of about 1 to 2 μ seconds. In this paper, a single-phase and three-phase bridge rectifier circuits’ power factors were modeled and simulated in MATLAB. The resulting voltage wave forms displayed in Figures 4 and 5 respectively showed that P.W.M method is a premier compared with other methods and is also better at low output voltage demand. Again, by using several pulses in each half cycle of the output voltage, it can reduce harmonic contents at low output voltages. In P.W.M, control is made independent of alpha (α), the firing angle of thyristor, and as such all the switching loss problems and electromagnetic interference (EMI) noise associated with switches/switching are totally eliminated here.

Keywords: Converters, harmonic contents, power factor correction, P.W.M method, single and three-phase bridge rectifier circuits, thyristors, transistors, noise and electromagnetic interference (EMI).

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INTRODUCTION

Pulse Width Modulation (P.W.M) is a form of modulation using a signal to modulate the width of pulses. The power amplifier for instance is switched according to the level of the modulating signal, passing current only when the output resistance is low. The width or duration of each pulse is proportional to the amplitude of the modulating signal at that instant. The system operates with a very high depth of modulation so that overall power efficiencies of about 70% can be achieved [1].

In P.W.M, the source voltage, V_s is placed across the load for a certain interval followed by shorting the load for the subsequent interval. P.W.M is a method of varying the mark-to-space ratio of the output voltage waveform during a cycle so as to minimize the magnitude of harmonics in the output [2]. PWM are of two types: (i) equal pulse width modulation and (ii) sinusoidal pulse width modulation.

Equal Pulse Width Modulation

In the modulation circuit, we have a voltage source V_s , a pair of self-commutating switches T_1, T_3 , a pair of line commutating switches T_2, T_4 and an applied load [4]. Figure-1 is a single-phase P.W.M rectifier circuit while (b) is its voltage and current waveforms.

Figure-2 consists of the voltage waveforms V_s ; the thyristor gate pulses ig_2 and ig_4 for the line commutated switches. Just below this is the dc reference signal V_r of variable magnitude while the triangular shapes are the triangular or carrier signal waves of fixed amplitude. Modulation is obtained by comparing the two signals in a comparator. The pulse width is changed by varying the magnitude of the dc reference signal. The dc reference signal also sets the output frequency, f_0 while the carrier or the triangular signal waves determine the pulse number P . While v_0 represent the output voltage waveform, i_s represent the source current [4].

Attributes of Multi-pulse or Equal P.W.M [4]

- Several equidistant pulses per cycle are used.
- Equal P.W.M is so-called because all the pulses have the same width for a given value of modulation index (m).
- Here, using several pulses in each half cycle of the output voltage can reduce the harmonic content.
- The method is a natural extension of the single pulse modulation and permits a reduction of harmonic content at low output voltages.

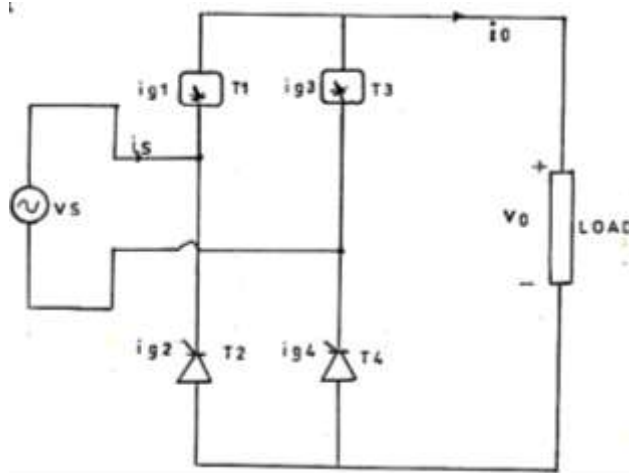


Fig-1: Single-phase pulse width modulated fully controlled rectifier circuit

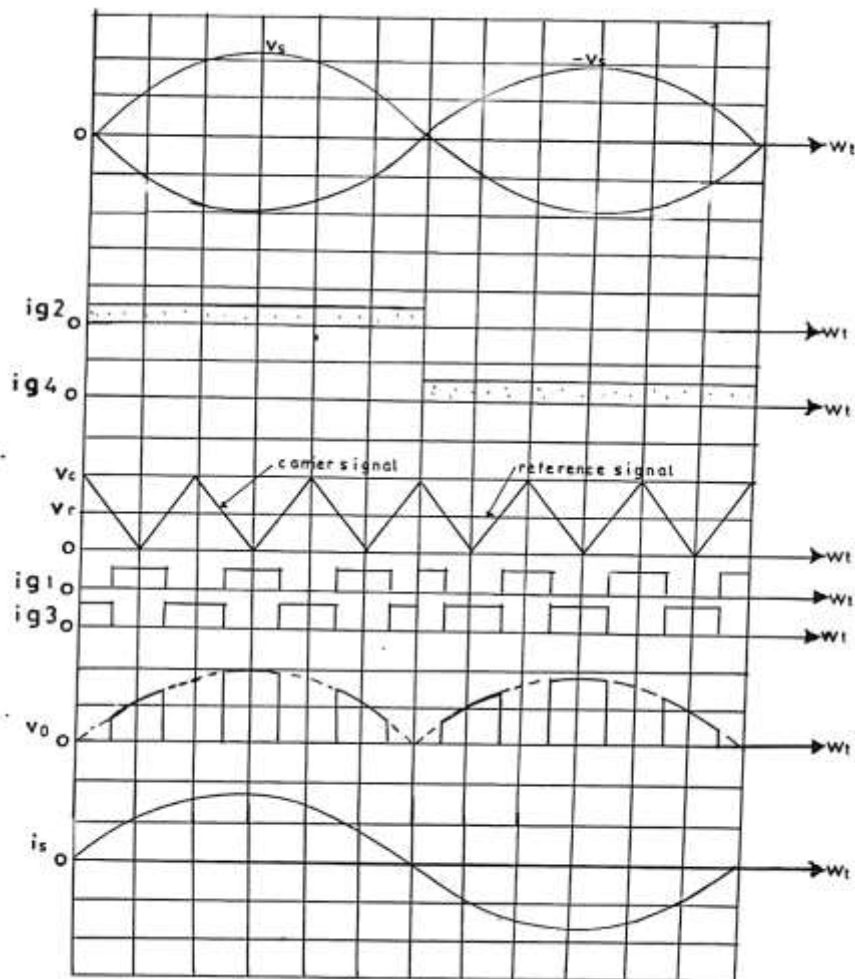


Fig-2: Its waveforms for rectification mode ($f_c = 6f_s$)

Sinusoidal Pulse Width Modulation

Similarly, for V_r in equal pulse width modulation, V_R can be a sinusoid in which case is called sinusoidal pulse width modulation. The method is so-called because the pulse width is a sinusoidal function of its angular position in the cycle. It is also called triangulation or P.W.M with natural sampling [4]. With V_R as rectified sinusoid, the graphs are repeated for $f_c = 6f_s$. Figure-3 is the waveform for rectification mode of a single-phase sinusoidal P.W.M rectifier with $f_c = 6f_s$; where f_c and f_s are the carrier and

source frequencies respectively. The figure shows that in sinusoidal P.W.M, the d.c modulating signal in equal P.W.M described in Figure-2 is replaced by a sinusoid in which case is called sinusoidal P.W.M [4].

The rectified sinusoid V_R is synchronized with source voltage V_s and has a variable amplitude V_{rm} . The carrier wave V_c also synchronized with a.c source voltage V_s , producing integer number of cycles in half cycle of V_s e.g. $f_c = 6f_s$.

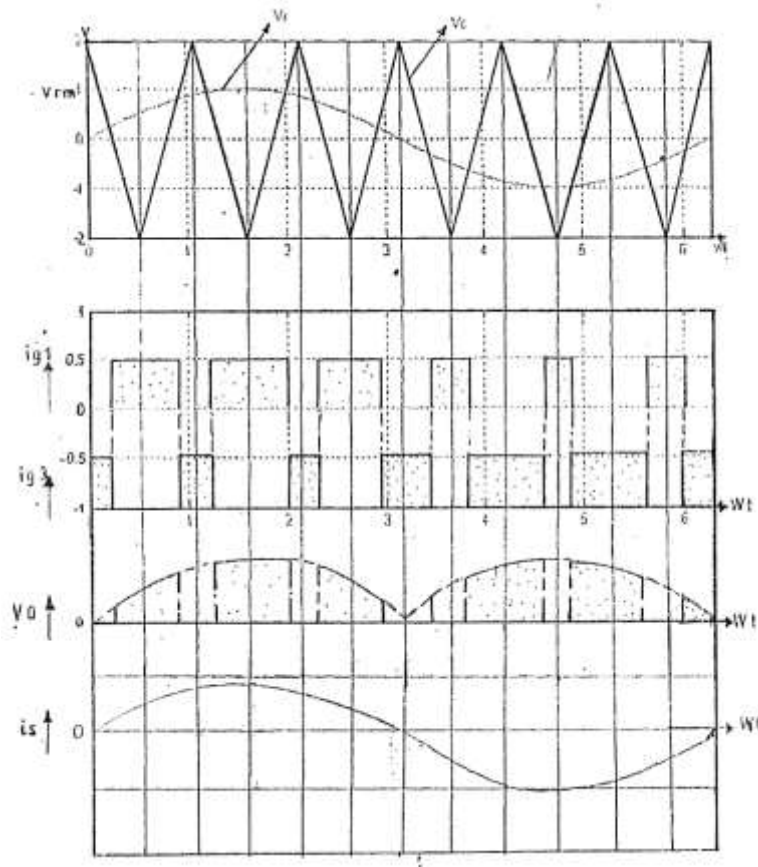


Fig-3: Waveforms for rectification mode of a single-phase bridge rectifier (sinusoidal P.W.M) $f_c = 6f_s$.

Attributes of Sinusoidal P.W.M [4]

- The d.c reference signal (in equal P.W.M) is replaced by a sinusoid in which case, is called sinusoidal P.W.M.
- Sinusoidal P.W.M is so-called because the pulse width is a sinusoidal function of its angular position in a cycle.
- The method is also called triangulation or P.W.M with natural sampling.

**Analysis and Power Factor Models
Single-phase Full Bridge, Voltage P.W.M Analysis [3]**

The modulation index m is

$$m = \frac{V_{rm}}{V_{cm}} \dots \dots \dots (3.1)$$

Where,

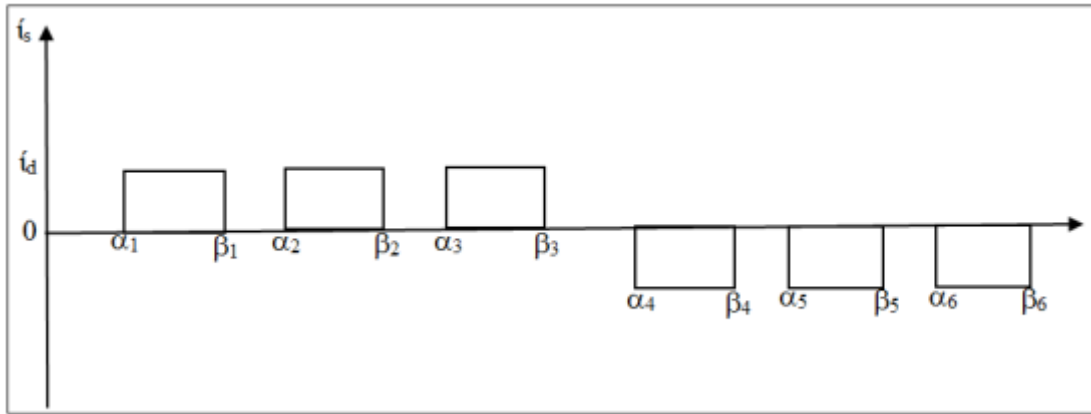
V_{rm} is amplitude of V_r
 V_{cm} is amplitude of V_c

For under modulation $0 \leq m \leq 1$
 With reference to Figure-1 [3].

If T_1 and T_2 are on,
 $i_s = i_0$

If T_3 and T_4 are on,
 $i_s = -i_0$

If T_1 and T_4 or T_3 and T_2 are on,
 $i_s = 0$



$$\left\{ \begin{aligned} \alpha_4 &= 2\pi - \beta_3 & \beta_4 &= 2\pi - \alpha_3 \\ \alpha_5 &= 2\pi - \beta_2 & \beta_5 &= 2\pi - \alpha_2 \\ \alpha_6 &= 2\pi - \beta_1 & \beta_6 &= 2\pi - \alpha_1 \end{aligned} \right\} \dots\dots\dots (3.2)$$

Slope for the negative slope section of the triangular wave looking at Figure-2 [3] is given as:

$$-\frac{V_{cm}}{\pi/N_p} = -\frac{N_p V_{cm}}{\pi} \dots\dots\dots (3.3)$$

Where,

- V_{cm} = amplitude of the carrier wave
- π/N_p = angular space occupied by half of the carrier signal
- N_p = pulse number

Its intercept at the y-axis is obtained as follows:

$$\left. \begin{aligned} 1^{st} \text{ negative sloped section, y intercept} &= V_{cm} \\ 2^{nd} \text{ negative sloped section, y intercept} &= 3V_{cm} \\ 3^{rd} \text{ negative sloped section, y intercept} &= 5V_{cm} \\ k^{th} \text{ intercept} &= (2k - 1)V_{cm} \end{aligned} \right\} \dots\dots\dots (3.4)$$

Hence, equation of the first negative sloped side of the carrier wave [3]

$$\left. \begin{aligned} Y_{1-} &= -\frac{V_{cm}}{\pi/N_p} X + V_{cm} \\ \Rightarrow Y_{1-} &= -\frac{N_p V_{cm}}{\pi} X + V_{cm} \\ \text{For the second, } Y_{2-} &= -\frac{N_p V_{cm}}{\pi} X + 3V_{cm} \\ \text{For the third, } Y_{3-} &= -\frac{N_p V_{cm}}{\pi} X + 5V_{cm} \\ \text{For the } k^{th}, Y_{k-} &= -\frac{N_p V_{cm}}{\pi} X + (2k - 1)V_{cm} \end{aligned} \right\} \dots\dots\dots (3.5)$$

Similarly, for the equations of positive sloped side of the carrier waves, we have [3]:

$$\left. \begin{aligned} Y_{1+} &= \frac{N_p V_{cm}}{\pi} X - V_{cm} \\ \text{For the second, } Y_{2+} &= \frac{N_p V_{cm}}{\pi} X - 3V_{cm} \\ \text{For the third, } Y_{3+} &= \frac{N_p V_{cm}}{\pi} X - 5V_{cm} \\ \text{For the } k^{th}, Y_{k+} &= \frac{N_p V_{cm}}{\pi} X - (2k - 1)V_{cm} \end{aligned} \right\} \dots\dots\dots (3.6)$$

Therefore, we have for positive and negative kth sloped section of the carrier wave:

$$Y_{K-} = -\frac{N_p V_{cm}}{\pi} X + (2k - 1)V_{cm} \dots\dots\dots (3.7)$$

$(1 \leq k \leq 3)$

$$Y_{K+} = \frac{N_p V_{cm}}{\pi} X - (2k - 1)V_{cm} \dots\dots\dots (3.8)$$

$(1 \leq K \leq 3)$

Since these equations cut the d.c reference axis (V_{rm}) at α_k and β_k we have:

$$V_{rm} = -\frac{N_p V_{cm}}{\pi} \alpha_k + (2k - 1)V_{cm} \dots\dots\dots (3.9)$$

$$\frac{V_{rm}}{V_{cm}} = -\frac{N_p}{\pi} \alpha_k + 2k - 1 \dots\dots\dots (3.10)$$

Where

$$\frac{V_{rm}}{V_{cm}} = \text{modulation index (m)} \dots\dots\dots (3.11)$$

$$\therefore m = -\frac{N_p}{\pi} \alpha_k + 2k - 1 \dots\dots\dots (3.12)$$

$$m + \frac{N_p}{\pi} \alpha_k - 2k + 1 = 0$$

$$\frac{N_p}{\pi} \alpha_k = 2k - 1 - m \dots\dots\dots (3.13)$$

$$\therefore \alpha_k = \frac{\pi}{N_p} [2k - 1 - m] [3] \dots\dots\dots (3.14)$$

Similarly, for Y_{k+}

$$\beta_k = \frac{\pi}{N_p} [2k - 1 + m] [3] \dots\dots\dots (3.15)$$

Power Factor Model for Single-phase Voltage P.W.M

For pair of pulse duration (α_k, β_k) and ($2\pi - \beta_k$), ($2\pi - \alpha_k$), the harmonic current, i_n is:
 $i_n = a_{kn} \cos n\omega t + b_{kn} \sin n\omega t$ (using Fourier series)

Where

$$a_{kn} = \left(\frac{1}{\pi}\right) \int_0^{2\pi} [i_{kn} \cos n\omega t] d\omega t \dots\dots\dots (3.16)$$

$$a_{kn} = \left(\frac{1}{\pi}\right) \left\{ \int_{\alpha_k}^{\beta_k} [i_{kn} \cos n\omega t] - \int_{2\pi-\beta_k}^{2\pi-\alpha_k} [i_{kn} \cos n\omega t] \right\} d\omega t \dots\dots\dots (3.17)$$

$$\therefore a_{kn} = 0$$

$$b_{kn} = \left(\frac{1}{\pi}\right) \int_0^{2\pi} [i_{kn} \sin n\omega t] d\omega t \dots\dots\dots (3.18)$$

$$b_{kn} = \left(\frac{1}{\pi}\right) \left\{ \int_{\alpha_k}^{\beta_k} [i_{kn} \sin n\omega t] - \int_{2\pi-\beta_k}^{2\pi-\alpha_k} [i_{kn} \sin n\omega t] \right\} d\omega t \dots\dots\dots (3.19)$$

$$b_{kn} = \frac{2id}{n\pi} \{ \cos n\alpha_k - \cos n\beta_k \} \dots\dots\dots (3.20)$$

$$i_{kn} = b_{kn} \sin n\omega t = \frac{2id}{n\pi} \{ \cos n\alpha_k - \cos n\beta_k \} \sin n\omega t$$

$$i_{sn} = \sum i_{kn} \Rightarrow \sum \frac{2id}{n\pi} \{ \cos n\alpha_k - \cos n\beta_k \} \sin n\omega t \dots\dots\dots (3.21)$$

$$i_{s1} = \frac{b_{k1}}{\sqrt{2}} \Rightarrow \sum \frac{\sqrt{2} id}{\pi} \{ \cos \alpha_k - \cos \beta_k \} \dots\dots\dots (3.22)$$

$$i_{sR} = \left[\sum_{n=1}^{\infty} i_{sn}^2 \right] \text{ and } i_s = \left[\sum_{n=1}^{\infty} i_{sn}^2 \right]^{1/2} \dots\dots\dots (3.23)$$

$$= \left\{ \sum_{n=1}^{\infty} \left[\sum \frac{2id}{\sqrt{2} n\pi} \{ \cos n\alpha_k - \cos n\beta_k \} \right]^2 \right\}$$

$$\therefore i_s = \left\{ \sum_{n=1}^{\infty} \left[\sum \frac{\sqrt{2} id}{n\pi} \{ \cos n\alpha_k - \cos n\beta_k \} \right]^2 \right\}^{1/2} \dots\dots\dots (3.24)$$

$$P.F. = \frac{i_{s1}}{i_s} \cos \theta_1 [3] \dots\dots\dots (3.25)$$

$$\therefore P.F. = \frac{\sum(\cos\alpha_k - \cos\beta_k)}{\left\{ \sum_{n=1}^{\infty} \left[\sum_{k=1}^{Np/2} \frac{1}{n} (\cos n\alpha_k - \cos n\beta_k) \right]^2 \right\}^{1/2}} \dots\dots\dots (3.26)$$

Where P.F. = power factor, Np = pulse number, α_k and β_k = where equation cut d.c reference axis.

Note that $\phi_1 = 0$ since cosine component of the harmonic amplitude is zero.

Three-phase Six-pulse Voltage P.W.M Analysis and Power Factor Models [3]

Similarly, the three-phase six-pulse voltage P.W.M analysis and power factor models were carried out and came up with [3]:

$$P.F. = \frac{\sum_{k=1}^2 (\cos\alpha_k - \cos\beta_k) + \cos\alpha_k}{\left\{ \sum_{n=1}^{\infty} \left[\frac{1}{n} \left(\sum_{k=1}^2 (\cos n\alpha_k - \cos n\beta_k) + \cos n\alpha_k \right) \right]^2 \right\}^{1/2}} \dots\dots\dots (3.27)$$

Where P.F. = Power Factor

$$\alpha_k = \alpha_0 - \frac{m \sin\alpha_0 + (12/\pi)\alpha_0 + (4k - 2)}{m \cos\alpha_0 + 12/\pi}$$

$$\beta_k = \beta_0 - \frac{m \sin\beta_0 - (12/\pi)\beta_0 + 4(k - 1)}{m \cos\beta_0 - 12/\pi}$$

$\alpha_0 = \beta_0$ = rough values of alpha and beta obtained graphically

m = modulation index, range = m (0 ≤ m ≤ 1)

k = variable with range, k (1 ≤ k ≤ 3)

n = harmonic order, range n = 1, 3, 5 ... ∞

SIMULATION/RESULTS

With the power factor expressions obtained in equations 3.26 and 3.27, Matlab [7] was employed in

programming and simulation of the single-phase and three-phase voltage P.W.M converters and the resulting curves are displayed in Figures 4 & 5 respectively.

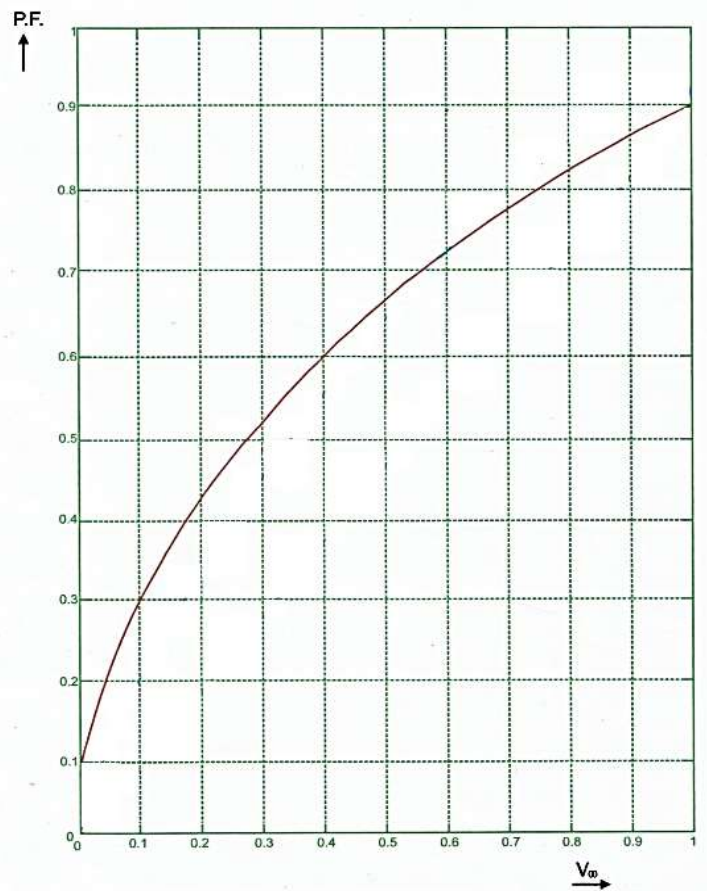


Fig-4: Plot of single-phase full-bridge P.W.M converter

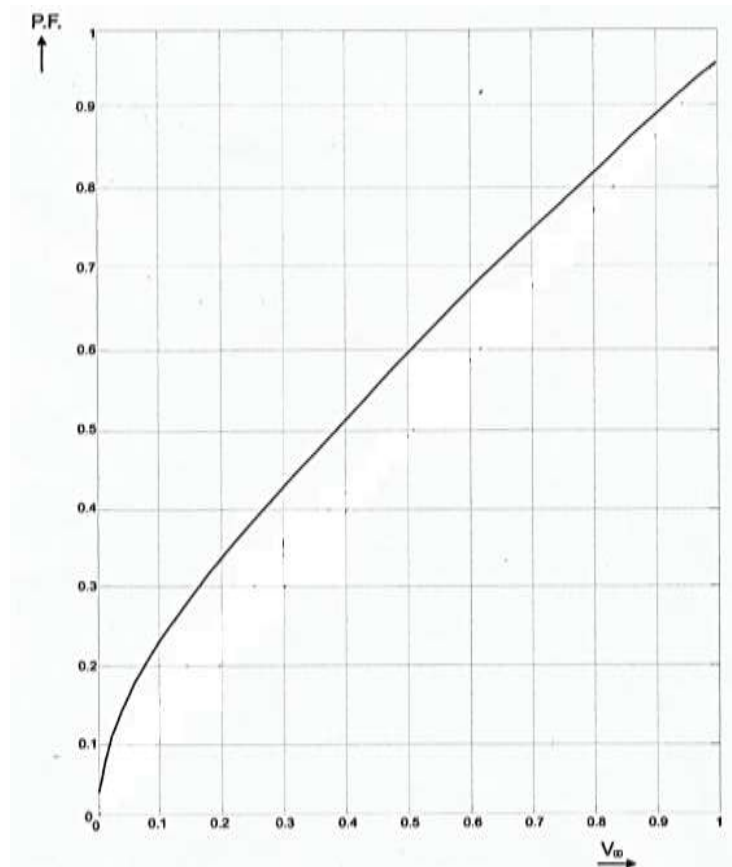


Fig-5: Plot of three-phase full-bridge P.W.M converter

ANALYSIS RESULTS

P.W.M in single or three-phase system is a premier when compared with other phase-controlled methods. It is better at low output voltage demand. Again, by using several pulses in each half cycle of the output voltage, it can reduce harmonic contents at low output voltages. Moreover, in P.W.M, control is made independent of alpha (α) or firing angle of the thyristor and so all the switching loss problems and electromagnetic interference (EMI) noise associated with switches/ switching are totally eliminated. Although P.W.M has all these qualities, the a.c input power factor still degenerate with decrease in the load voltage. Therefore, both P.W.M and other phase control methods are tolerable where nonlinear load concentration is low.

CONCLUSION

Since harmonics decrease with the increase in rectifier pulse numbers, the three-phase six-pulse rectifiers have lower amplitude of harmonics than the single-phase two-pulse rectifiers and therefore have better power factors. Again, P.W.M is better at low output voltage demand and by using several pulses in each half cycle of the output voltage it can reduce harmonic contents at low output voltages. Although all these good qualities are abundant in P.W.M the ac input

power factor still degenerate with decrease in load voltage and therefore P.W.M and other phase control methods are tolerable where nonlinear load concentration is low.

REFERENCES

1. Collins. Dictionary of electronics (Definitions for Digital Age). Davidson Pre-press Graphics Limited, Glasgow. 2004.
2. Dubey GK. Power semiconductor-controlled drive, Prentice Hall International Edition. 1989.
3. Ibekwe BE. Comparative study of converter power factor correction techniques. Masters Dissertation, Enugu State University of Science and Technology (ESUT), Enugu. 2010; 70-76.
4. Ibekwe BE, Eneh II, Ude IJ. Guiding Principles in Selecting AC to DC Converters for Power Factor Correction in AC Transmission system. IJERA Journal. 2014 Oct;4(10).
5. Ogbuefi UC. A power flow analysis of Nigerian power system with compensation on some buses. Ph.D Thesis, University of Nigeria, Nsukka. 2013.
6. Vanguard Newspaper. Power situation of Nigeria. 2016; (pp.12).
7. Okoro OI. Introduction to Matlab/Simulink for Engineers and Scientists. John Jacob's Classic Publishers Limited, Enugu. 2005.